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magnetoresistors are coupled to the first isolator output terminal, wherein the second and third magnetoresistors are coupled to the first supply terminal, wherein the third and fourth magnetoresistors are coupled to the second isolator output terminal, and wherein the first and fourth magnetoresistors are coupled to the second supply terminal; and,

an input strap having at least one turn coupled between the first and second isolator input terminals, wherein the input strap is disposed with respect to the first, second, third, and fourth magnetoresistors so that a magnetic field is generated over two of the magnetoresistors in one direction, so that a magnetic field is generated over the other two of the magnetoresistors in an opposite direction.

Please add the following claim:

31. The integrated signal isolator of claim 1 wherein the input strap is disposed with respect to the first, second, third, and fourth magnetoresistors so that, when input current flows between the first and second isolator input terminals, a resistance of the first magnetoresistor tracks a resistance of the third magnetoresistor, and a resistance of the second magnetoresistor tracks a resistance of the fourth magnetoresistor.

REMARKS

Claims 1-31 are now in the application.

Applicant hereby elects claims 1-17 with traverse. Independent claims 1 and 26 cannot effectively be examined separately and, therefore, should be examined together.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached version is captioned **"VERSION WITH MARKINGS TO SHOW CHANGES MADE."**